

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	775	(716/16,17).CCLS.	USPAT; USOCR	OR	OFF	2006/01/11 09:48
L2	505751	fpga or (programmable near3 (array or logic or device or field or gate)) or soc or system near3 chip or fipsoc or asic or application near3 specific	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/11 09:52
L3	400430	fpga or (programmable near3 (array or logic or device or field or gate)) or soc or system near3 chip or fipsoc or asic or application near3 specific and resource\$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/11 09:52
L4	397179	fpga or (programmable near3 (array or logic or device or field or gate)) or soc or system near3 chip or fipsoc or asic or application near3 specific and resource\$2 and available and (type or kind)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/11 09:53
L5	427436	fpga or (programmable near3 (array or logic or device or field or gate)) or soc or system near3 chip or fipsoc or asic or application near3 specific and resource\$2 and available and (type or kind) and quantity or amount and allocat\$4 and display\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/11 09:55
L6	602	1 and 5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/11 09:54
L7	173	1 not 6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/11 09:55
L8	0	6 not 1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/11 09:55
L9	12615	(fpga or (programmable near3 (array or logic or device or field or gate)) or soc or system near3 chip or fipsoc or asic or application near3 specific and resource\$2 and available and (type or kind) and quantity or amount and allocat\$4 and display\$4).clm	USPAT	OR	ON	2006/01/11 09:56

L10	12267	(fpga or (programmable near3 (array or logic or device or field or gate) or soc or system near3 chip or fipsoc or asic or application near3 specific and resource\$2 and available and (type or kind) and (quantity or amount) and allocat\$4 and display\$4).clm.	USPAT	OR	ON	2006/01/11 09:56
L11	12267	(fpga or (programmable near3 (array or logic or device or field or gate) or soc or system near3 chip or fipsoc or asic or application near3 specific and resource\$2 and available and (type or kind) and (quantity or amount) and allocat\$4 and display\$4 and component and implement\$6 and remaining).clm.	USPAT	OR	ON	2006/01/11 09:57
L12	12267	(fpga or (programmable near3 (array or logic or device or field or gate) or soc or system near3 chip or fipsoc or asic or application near3 specific and resource\$2 and available\$6 and (type or kind) and (quantity or amount) and allocat\$4 and display\$4 and (system near3 component) and implement\$6 and remaining).clm.	USPAT	AND	ON	2006/01/11 10:04
L13	0	((fpga or soc or system near3 chip or fipsoc) and resource\$2 and available\$6 and (type or kind) and (quantity or amount) and allocat\$4 and display\$4 and (system near3 component) and implement\$6 and remaining).clm.	USPAT	AND	ON	2006/01/11 10:05